

Electronic Acknowledgement Receipt

EFS ID:	2316617
Application Number:	10764003
International Application Number:	
Confirmation Number:	8941
Title of Invention:	Method and system for phase offset cancellation in multi-phase clock generator circuits
First Named Inventor/Applicant Name:	Ramin Farjad-rad
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Filer:	Joseph A. Sawyer
Filer Authorized By:	
Attorney Docket Number:	3003P
Receipt Date:	12-OCT-2007
Filing Date:	22-JAN-2004
Time Stamp:	18:44:50
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Drawings-only black and white line drawings	scan05356.pdf	86075	no	1
			e9c8ce88f8d493f97b52b7a884bf5db274d276bd		

Warnings:

Information:

2	Drawings-only black and white line drawings	scan05357.pdf	90363	no	1
			5dd7480717b43a5a6a792d328794331296640d98		

Warnings:

Information:

Total Files Size (in bytes):			176438
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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